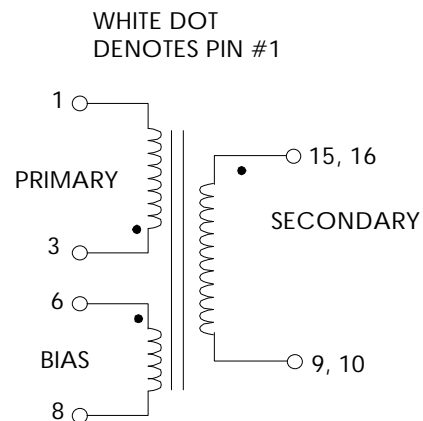


TABLE 1: ELECTRICAL SPECIFICATIONS AT 25 °C

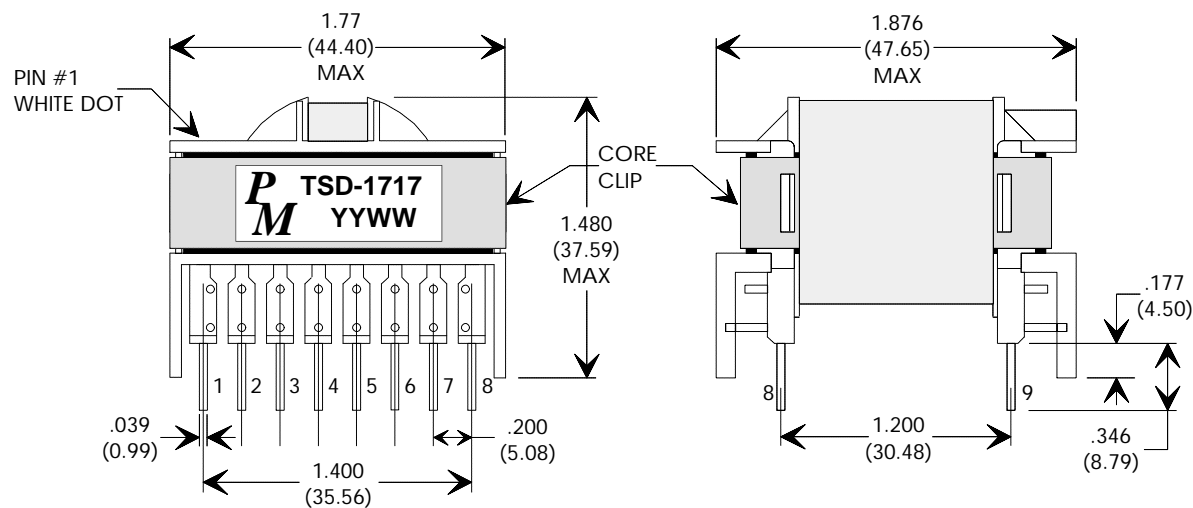
PARAMETER	SPEC LIMITS			UNITS
	MIN.	TYP.	MAX.	
PRIMARY INDUCTANCE (3-1) VOLTAGE = 0.250Vrms FREQUENCY = 100 KHZ	147	163	179	μHy
URNS RATIOS: SEC (15/16 - 9/10) : PRI (3-1) BIAS (6-8) : PRI (3-1)	-----	1 : 4.60 1 : 7.67	-----	± 2% ± 2%
PRI LEAKAGE IND. (SEC SHORTED) VOLTAGE = 0.250Vrms FREQUENCY = 100 KHZ	-----	-----	2.0	μHY
HI-POT: PRIMARY & BIAS, TO SECONDARY BIAS TO PRIMARY	3000 300	----- -----	----- -----	Vrms Vrms

FIGURE 1: SCHEMATIC DIAGRAM

*** PIN #15 TO #16 AND #9 TO #10
NEED TO BE CONNECT ON PCB
FOR HANDLE HIGH CURRENT**

NOTE1:

- A) ALL MATERIALS MEET "UL", "CSA" & "IEC" REQUIREMENTS
 B) ALL MATERIAL RATED 130 °C OR BETTER.
 D) VARNISH FINISHED ASSEMBLY.

FIGURE 2: PHYSICAL DIMENSIONS IN INCHES, (mm)

ETD-39, 16-PIN BOBBIN



UNLESS OTHERWISE SPECIFIED
 DIMENSIONS ARE IN INCHES
 DIMENSIONAL TOLERANCES ARE:
 DECIMALS ANGLES
 .XX ± .05 ± 0° 30'
 .XXX ± .020
 DO NOT SCALE DRAWING

REV.	DESCRIPTION OF CHANGES	BY
11/28/01	ORIGINAL RELEASE	LL
02/01/02	UPDATED LEAKAGE IND. TEST LIMIT, DIM., NUMBER TURNS ON SCHEM.	MP

TRANSFORMER CONTROL DRAWING

PREMIER P/N: TSD-1717	REVISION: 02/01/02
DRAWN BY: LINH LE	REF:
SCALE: NONE	SHEET: 1 OF 3