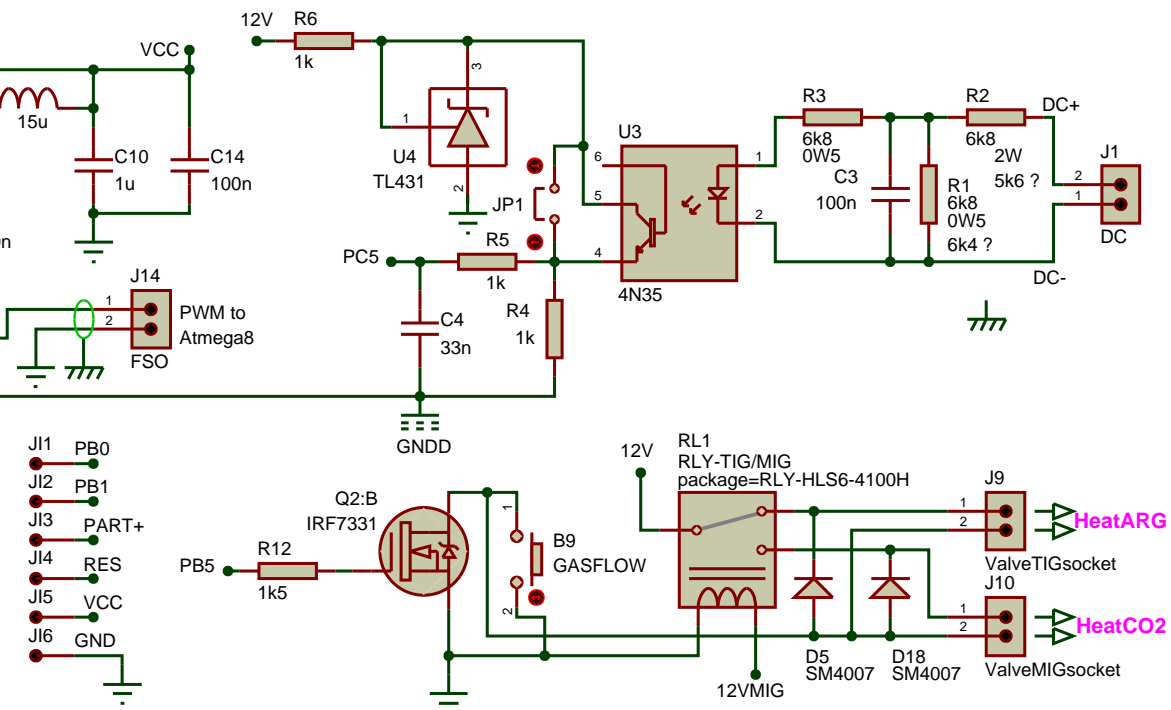
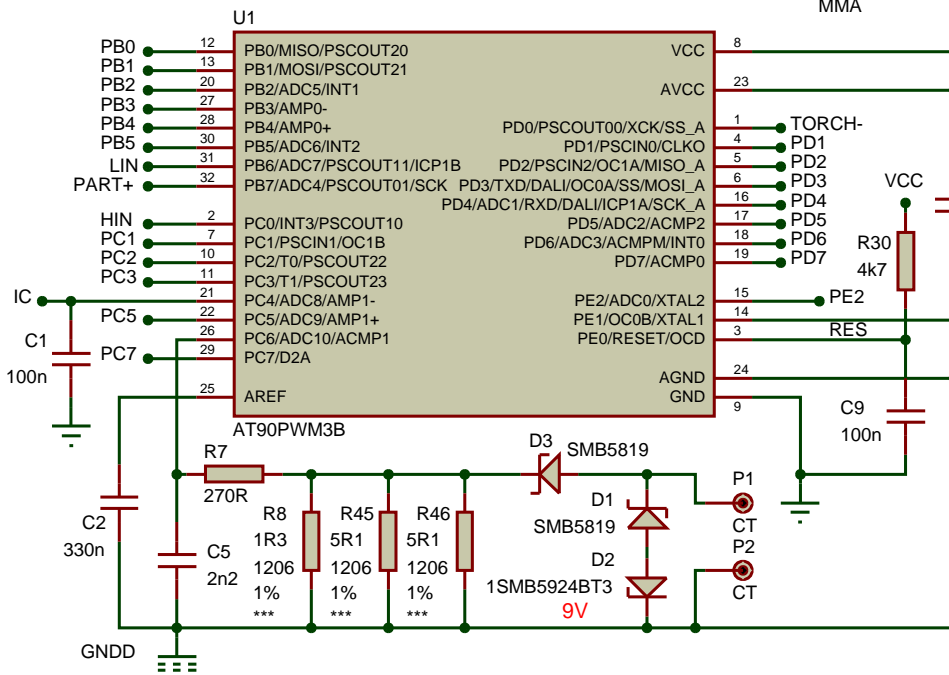
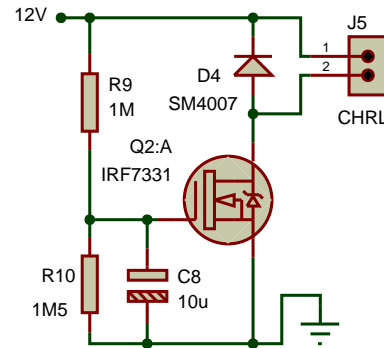
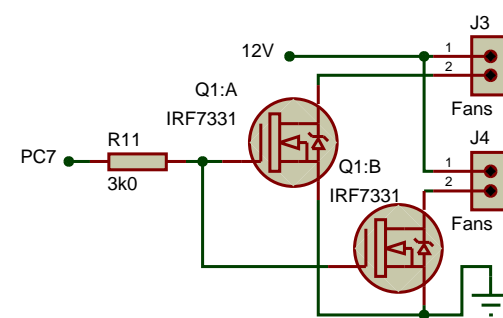
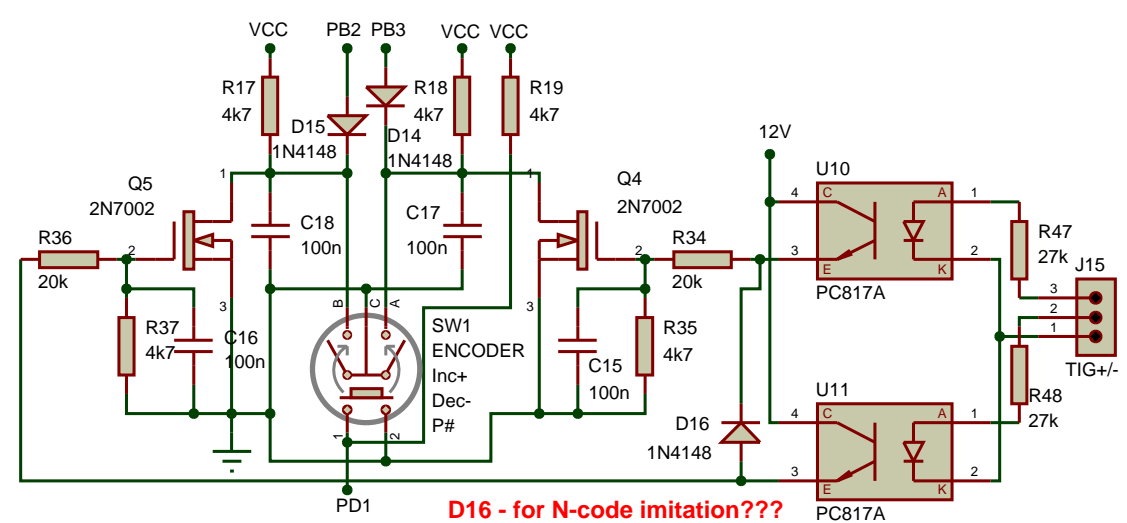
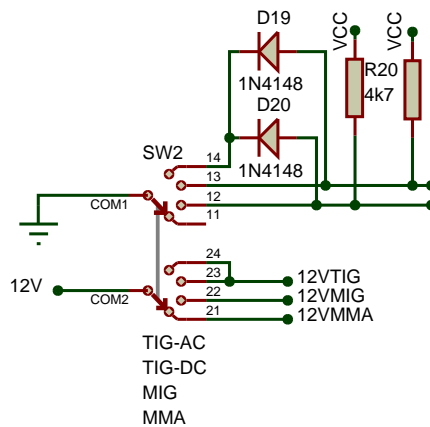
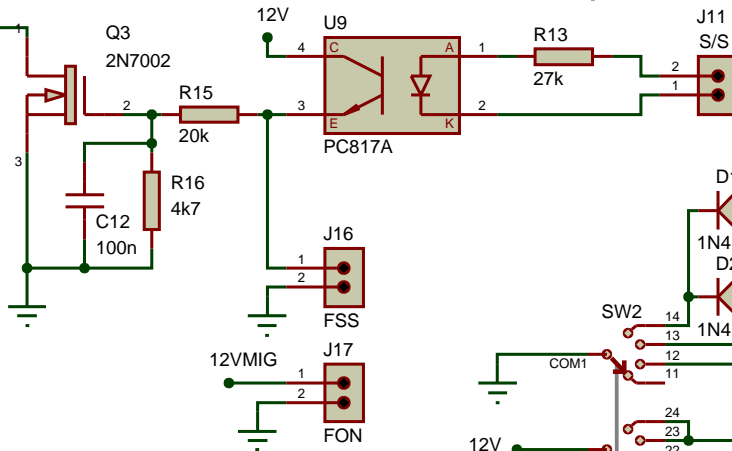
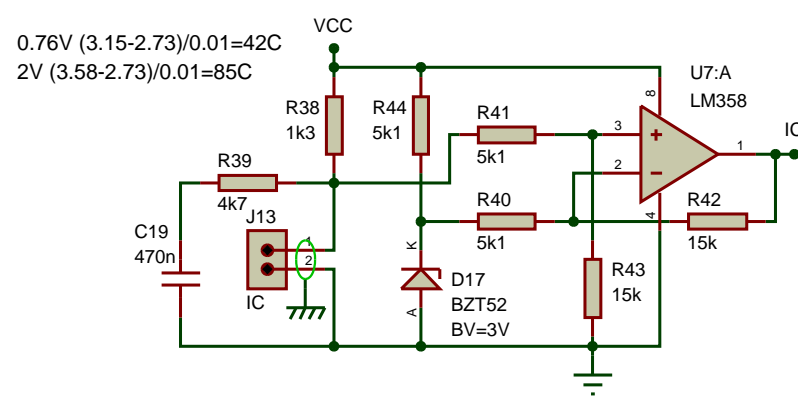
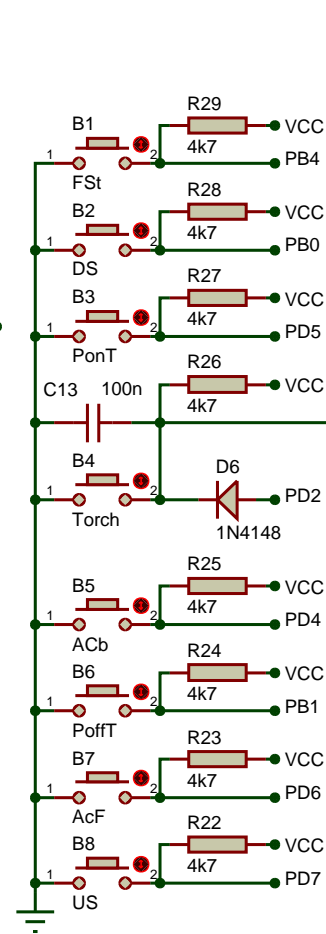
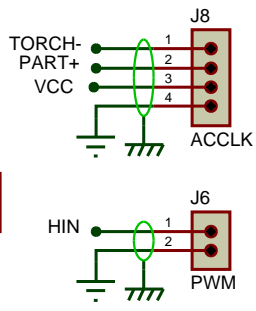
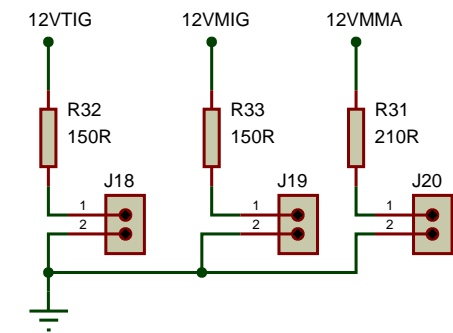
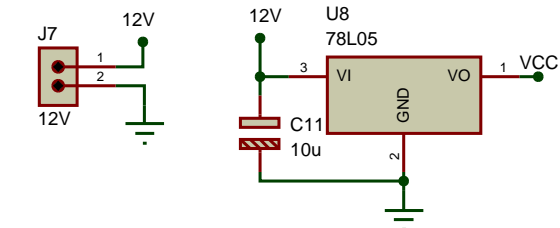


The circuit diagram illustrates a 4-bit parallel adder. It consists of two 74HC164 shift registers, U5 and U6, which are configured to perform a 4-bit parallel addition. The inputs to the adder are PC1, PC2, and PC3. The output of the adder is connected to a 7-segment display, which shows the hexadecimal digit 'A' (10101010). The display is labeled with segments A through G and DP. The circuit also includes a 330nF capacitor (C7) and a 5V power supply (VCC2).



1. - ,
2.) IC pwm
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